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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

MAY 31 2006

Complete if Known

Application Number	10/806,633
Filing Date	03-22-2004
First Named Inventor	Dimpsey et al.
Examiner Name	Padmanabhan, Mano
Art Unit	2188
Attorney Docket No.	AUS920040062US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
GP AA	5394529	02-28-1995	Brown, III et al.	
AB	6233679 B1	05-15-2001	Holmberg	
AC	5887159	03-23-1999	Burrows	
AD	6662295 B2	12-09-2003	Yamaura	
AE	5938760	08-17-1999	Levine et al.	
AF	6189072 B1	02-13-2001	Levine et al.	
AG	5276833	01-04-1994	Auvinen et al.	
AH	5287481	02-15-1994	Lin	
AI	5537572	07-16-1996	Michelsen et al.	
AJ	5805879	09-08-1998	Hervin et al.	
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AL	6735666 B1	05-11-2004	Koning	
AM	2004/0153612 A1	08-05-2004	Mutz et al.	
AN	2005/0091456 A1	04-28-2005	Huck	
GP AO	2005/0108483 A1	05-19-2005	Bungo	

FOREIGN PATENT DOCUMENTS

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GP AP	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.

Examiner: /Gary Portka/

Date Considered: 11/29/2006

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10806633
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	First Named Inventor	Dimpsey et al.	
	Art Unit	2188	
	Examiner Name	Moore, Patrick M.	
	Attorney Docket Number	AUS920040062US1	

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GP	1	6973417	B1	2005-12-06	Maxwell et al.			
GP	2	6721875	B1	2004-04-13	McCormick et al.			
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GP	1	5966538		1999-10-12	Granston et al.	
	2	5940618		1999-08-17	Blandy et al.	
	3	6189142	B1	2001-02-13	Johnston et al.	
	4	5768500		1998-06-16	Agrawal et al.	
	5	6026235		2000-02-15	Shaughnessy	
	6	5950003		1999-09-07	Kaneshiro et al.	
GP	7	6594820	B1	2003-07-15	Ungar	

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GP	1	20020073406	A1	2002-06-13	Gove	
↓	2	20030066055	A1	2003-04-03	Spivey	
	3	20030131343	A1	2002-07-10	French et al.	
GP	4	20040268316	A1	2004-12-30	Fisher et al.	

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GP	1	5761103		1998-06-02	Oakland et al.	
	2	6549959	B1	2003-04-13	Yates et al.	
	3	6772322	B1	2004-08-03	Merchant et al.	
	4	5937437		1999-08-10	Roth et al.	
	5	6070009		2000-05-30	Dean et al.	
	6	6381679	B1	2002-04-30	Matsubara et al.	
	7	6687794	B2	2004-02-03	Malik	
GP	8	6539458	B2	2003-03-25	Holmberg	

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GP	9	6973542	B1	2005-12-06	Schmuck et al.	
	10	6782454	B1	2004-08-24	Damron	
	11	5652858		1997-06-29	Okada et al.	
	12	6848029	B2	2005-01-25	Coldewey	
GP	13	5822790		1998-10-13	Mehrotra	

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GP	1	"Method for the dynamic prediction of nonsequential memory accesses", September 25, 2002, pp. 1-4, ip.com IPCOM000009888D.	<input type="checkbox"/>
GP	2	"Cache Miss Director - A Means of Prefetching Cache Missed Lines", August 1, 1982, IBM Technical Disclosure Bulletin, Vol. 25, Issue 3A, pp.1286	<input type="checkbox"/>

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